IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

N. KOBAYASHI, et al.

Application No.:

Rule 1.53(b) Continuation of U.S. Patent Application

Serial No. 10/205,426, filed July 26, 2002

Filed:

On Even Date Herewith

For:

NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

AND SEMICONDUCTOR DISK APPARATUS

Art Group of Parent:

2818

Examiner of Parent:

H. Hoang

<u>UNDER 37 CFR §1.97 & 1.98</u>

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

February 26, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO/SB/08A which list documents cited in parent applications Serial No. 10/205,426, filed July 26, 2002, which is a divisional of parent application Serial No. 09/758,221, filed January 12,

Docket No.: 520.39458CV2

2001 (now U.S. Patent No. 6,445,615), the entire disclosure of which is hereby incorporated by reference.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, Deposit Account No. 01-2135 (520.39458CV2).

Respectfully submitted,

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Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO				Complete if Known		
				Application Number	Not assigned yet	
	INFORMATION DISC	-OS	JRE	Filing Date	On even date herewith	
	STATEMENT BY APP	LIC	ANT	First Named Inventor	N. KOBAYASHI, et al.	
				Art Unit	Not assigned yet	
(use as many sheets as necessary)				Examiner Name	Not assigned yet	
Sheet	1	of	1	Attorney Docket Number	520.39458CV2	

	,	•	U.S. PATENT D	OCUMENTS		
-	Cite No.1	Document Number	<u> </u>	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where	
Examiner Initials'		Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY		Relevant Passages or Relevant Figures Appear	
		5,654,912	0819/97	Hasegawa et al.		
		5,603,001	02/1997	Sukegawa et al.		
		6,434,034	08-2002	Wallace et al.		
		6.078.520	06-2000	Tobita et al.		
						

FOREIGN PATENT DOCUMENTS						
Examiner Initials'	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			T⁵
		JP 10-63442	03/1998			
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OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS					
Examiner Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
		Kawahara et al., "20-Mb/s Erase/Record Flash Memory by Asymmetrical Operation" Symposium on VLSI Circuits Digest of Technical Papers, 1996, pp. 174-175			

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